Job Opening: DSP Engineer
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Qualifications:
The successful candidate has demonstrated the ability to delve deeply into challenging DSP problems and then develop creative, innovative, and practical solutions. The successful candidate has also demonstrated the ability to implement these solutions in HW and SW.

Job Requirements:
- Ph.D.
- Strong familiarity with
  - Multi-rate signal processing
  - Linear systems theory
  - Statistical signal processing
  - Digital filter design
  - Matlab DSP tools
- Experience implementing DSP in FPGA, GPU, or ASIC hardware
- Strong communication skills and the ability to work closely with a multi-disciplinary team of peers to define and implement new solutions.

Other valuable skills include:
- PC board or ASIC design experience
- RF or microwave analog design experience
- Experience with VHDL or Verilog
- Laboratory experience with test and measurement instruments and their associated DSP flows

Job Description:
Join an extremely talented and multi-disciplinary team uniquely chartered with inventing and developing novel measurement technologies for Agilent’s businesses. The team comprises experts in fields ranging from photonics and microwaves to FPGA design and software engineering. Typically, we work on a variety of research projects aimed at one or more of the following:
- providing differentiated performance improvements (> 10X) in key specifications
- enabling market disruption with totally new measurement capabilities
- positioning Agilent at the forefront of technology trends

As a member of the team, you will be asked to put your expertise to work in a variety of ways:
- Identify research opportunities with the potential to significantly impact Agilent’s businesses
- Develop practical solutions to address those or other opportunities
- Work with a small team to execute project plans enabling successful technology transfer to Agilent’s businesses; this typically requires hardware implementation and extensive risk-reduction.

Your project assignments may be DSP-centric (perhaps self-initiated) or they may involve DSP in support of other technologies developed by the team.

To foster collaboration and innovation within the team, this job must be performed from our offices in Santa Clara, California. Organizationally, this position is part of the Measurement Research Laboratory (MRL) in TLO.

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